EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	57	323/284.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:26
L2	41	323/285.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:30
L3	13	361/62.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:32
L4	5	361/63.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:34
L5	3	307/151.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:34
L6	7	307/82.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:35
L7	2	307/44.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:35

L8	0	(("dc-dc" "dc/dc") adj converter) with ((protection isolation protect isolate) adj (switch transistor fet mosfet)) with (cell module) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:36
L9	1	(("dc-dc" "dc/dc") adj converter) with ((protection isolation protect isolate) adj (switch transistor fet mosfet)) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:36
L10	28	361/18.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:36
L11	10	361/18.ccls. and (("dc-dc" "dc/dc") adj converter) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:37
L12	1	361/101.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:37
L13	6	307/45.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:37
L14	2	307/58.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:38
L15	58	714/48.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:38

L16	28	323/272.ccls. and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:39
L17	1826	((dc "dc-dc" "dc/dc") adj converter) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:39
L18	70	((buck$boost) near2 converter) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:40
L19	60	((buck$boost) adj converter) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:40
L20	1	(reversible adj converter) and @pd>="20071016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/14 11:40
L21	1	((high-voltage (high adj voltage)) and (low-voltage (low adj voltage)) and cell and (converter chopper) and (switch switching) and control and (duty adj ratio) and (mos mosfet fet) and inductor and (intrinsic adj diode) and anode and cathode).clm.	US-PGPUB	OR	ON	2008/03/14 11:43
L22	1	((high-voltage (high adj voltage)) and (low-voltage (low adj voltage)) and cell and (converter chopper) and (switch switching) and control and (duty adj ratio) and (mos mosfet fet) and inductor and (intrinsic adj diode)).clm.	US-PGPUB	OR	ON	2008/03/14 11:43

| L23 | 1 | ((high-voltage (high adj voltage)) and (low-voltage (low adj voltage)) and (converter chopper) and (switch switching) and control and (duty adj ratio) and (mos mosfet fet) and inductor and (intrinsic adj diode)).clm. | US-PGPUB | OR | ON | 2008/03/14 11:43 |
| L24 | 1 | ((converter chopper) and (switch switching) and control and (duty adj ratio) and (mos mosfet fet) and inductor and (intrinsic adj diode)).clm. | US-PGPUB | OR | ON | 2008/03/14 11:43 |

3/14/2008 11:53:57 AM
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